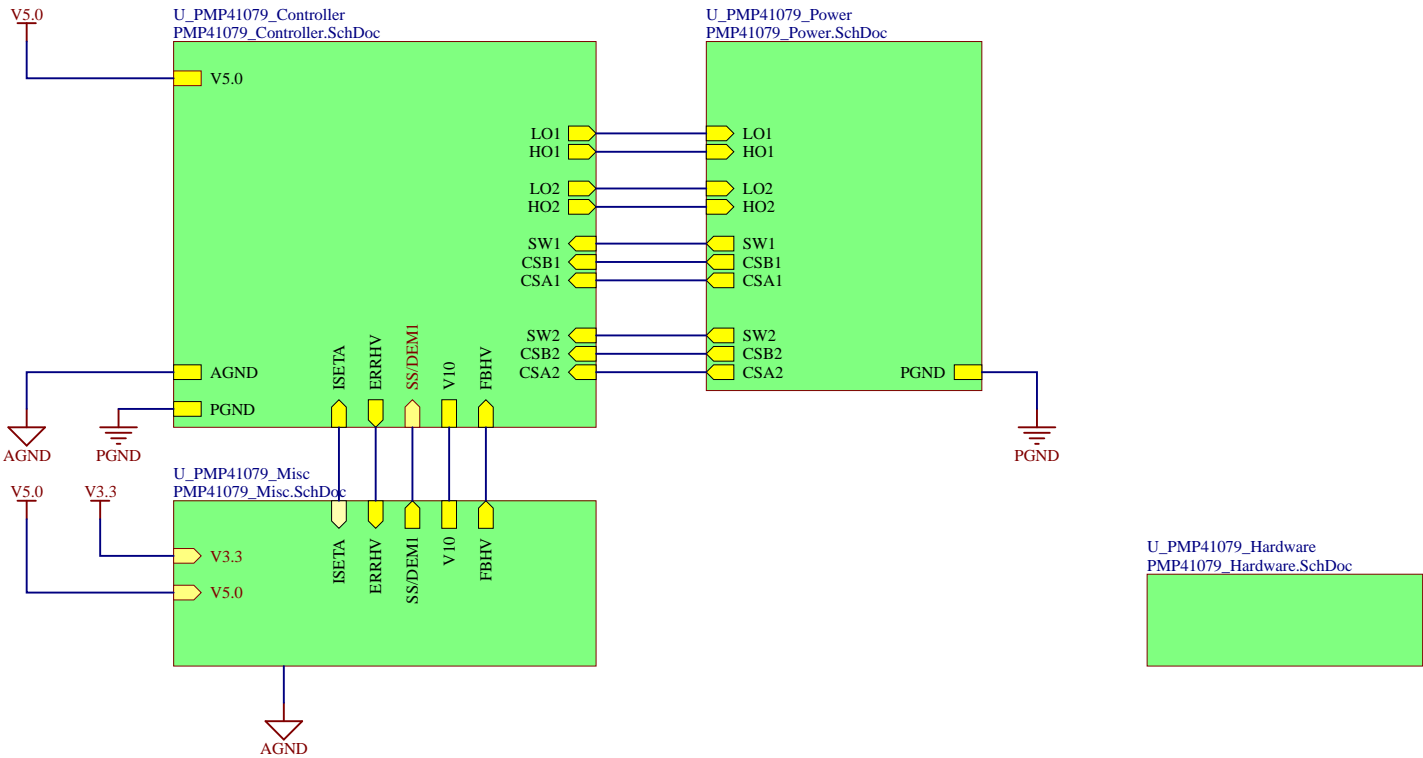
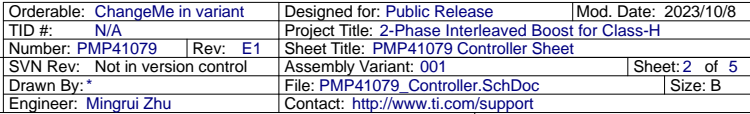


Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

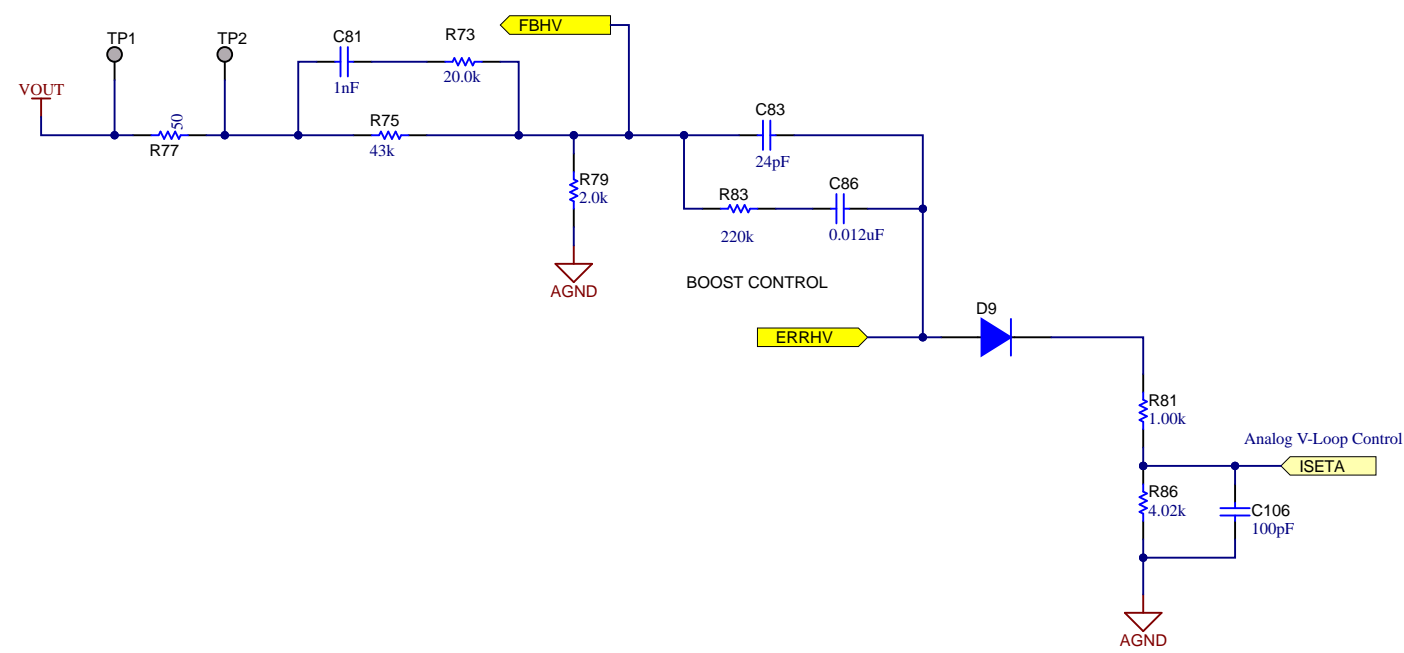


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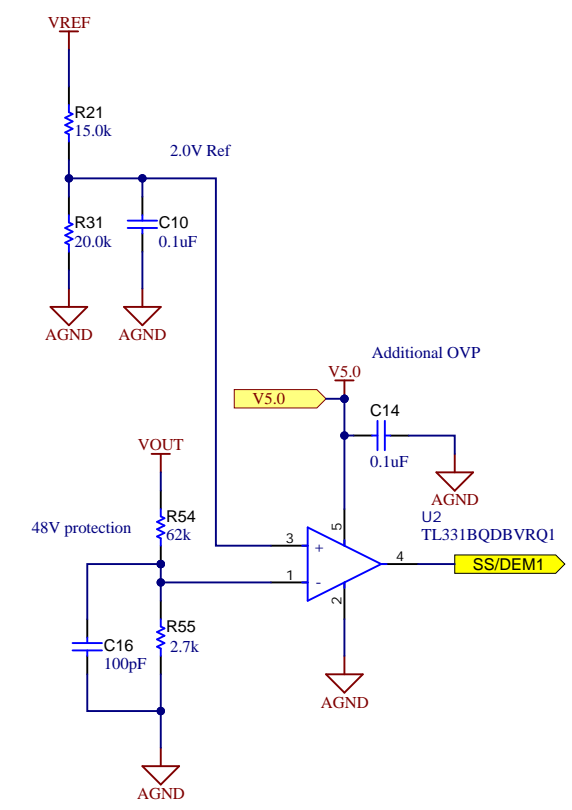
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Number: <a href="#">PMP41079</a>	Rev: <a href="#">E1</a>	Sheet Title: <a href="#">PMP41079 Cover Sheet</a>
SVN Rev: <a href="#">Not in version control</a>	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">1</a> of <a href="#">4</a>
Drawn By:	File: <a href="#">PMP41079-Cover.SchDoc</a>	Size: B
Engineer: <a href="#">Mingrui Zhu</a>	Contact: <a href="#">http://www.ti.com/support</a>	



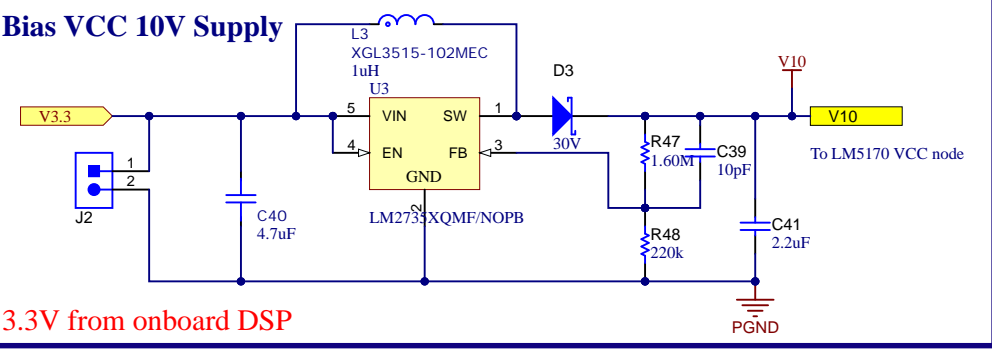
V-Loop



Additional OVP for HV Rail



Bias VCC 10V Supply

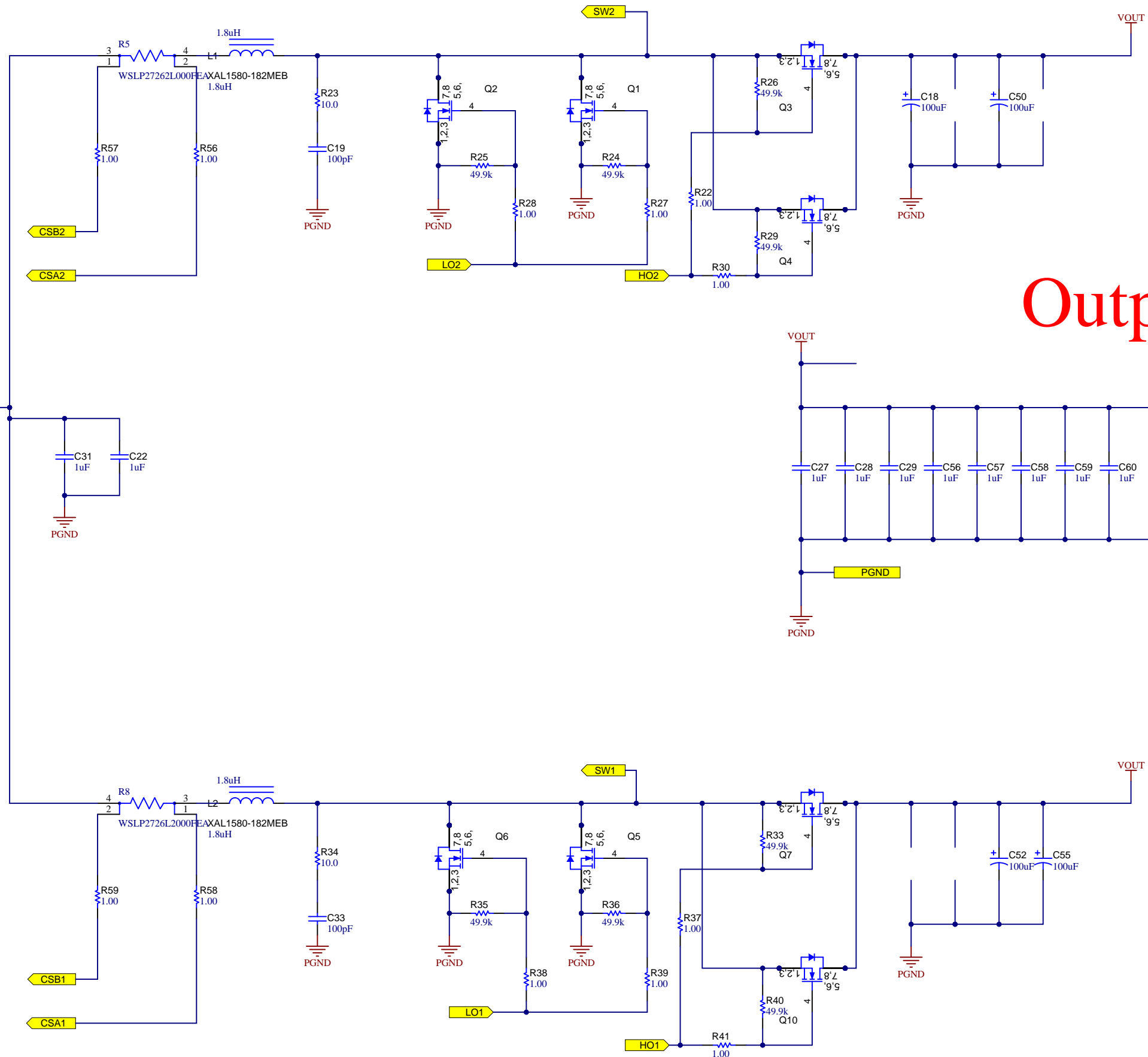
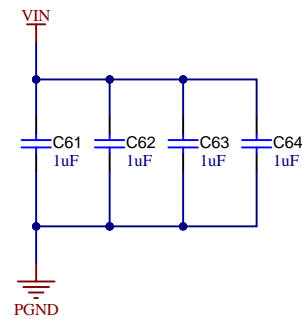
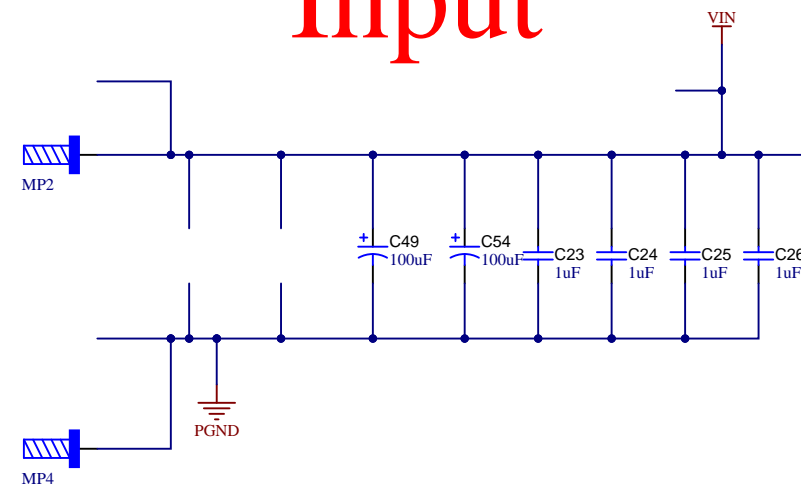


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Number: <a href="#">PMP41079</a>	Rev: <a href="#">E1</a>	Sheet Title: <a href="#">PMP41079 Bias+Feedback+Protection</a>
SVN Rev: <a href="#">Not in version control</a>	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">3</a> of <a href="#">5</a>
Drawn By: <a href="#">Mingrui Zhu</a>	File: <a href="#">PMP41079_Misc.SchDoc</a>	Size: <a href="#">B</a>
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# Input

# Output



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Number: <a href="#">PMP41079</a>	Rev: <a href="#">E1</a>	Sheet Title: <a href="#">PMP41079 Power Stage Sheet</a>
SVN Rev: Not in version control	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">4</a> of <a href="#">5</a>
Drawn By:	File: <a href="#">PMP41079_Power.SchDoc</a>	Size: <a href="#">B</a>
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PCB Number: PMP41079  
PCB Rev: E1

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Engineer: <a href="#">Mingrui Zhu</a>	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>		<a href="http://www.ti.com">http://www.ti.com</a> © Texas Instruments 2023